

METHOD AND APPARATUS FOR RESETTING
A HIGH SPEED LATCH CIRCUIT

ABSTRACT OF THE DISCLOSURE

5 A latch circuit includes first and second resonant
tunneling diodes coupled in series, and a reset portion
having a photodiode portion responsive to varying
photonic energy for switching between first and second
states which are different. When the photodiode portion
is in its first state, the reset portion normalizes a
voltage across each of the resonant tunneling diodes.

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